

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JOSEPH HUANG ET AL.

Filed: JANUARY 15, 2004

For: APPARATUS AND METHODS FOR PROVIDING REDUNDANCY IN
INTEGRATED CIRCUITS

Serial No.: 10/757,928

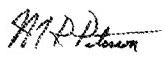
Group Art Unit: 2827

Examiner: WEINBERG, MICHAEL J.

Atty Dkt: ALTR:023

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Date: July 20, 2006

Name: 

Commissioner for Patents
P.O. Box 1450
Alexandria 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

This paper is submitted in response to the Office Action dated March 21, 2006 (“Office Action”). Concurrently with this paper, the Applicant files a request for an extension of time.